

asureVIP from TVS

TVS MIPI RFFE OVM/UVM VIP

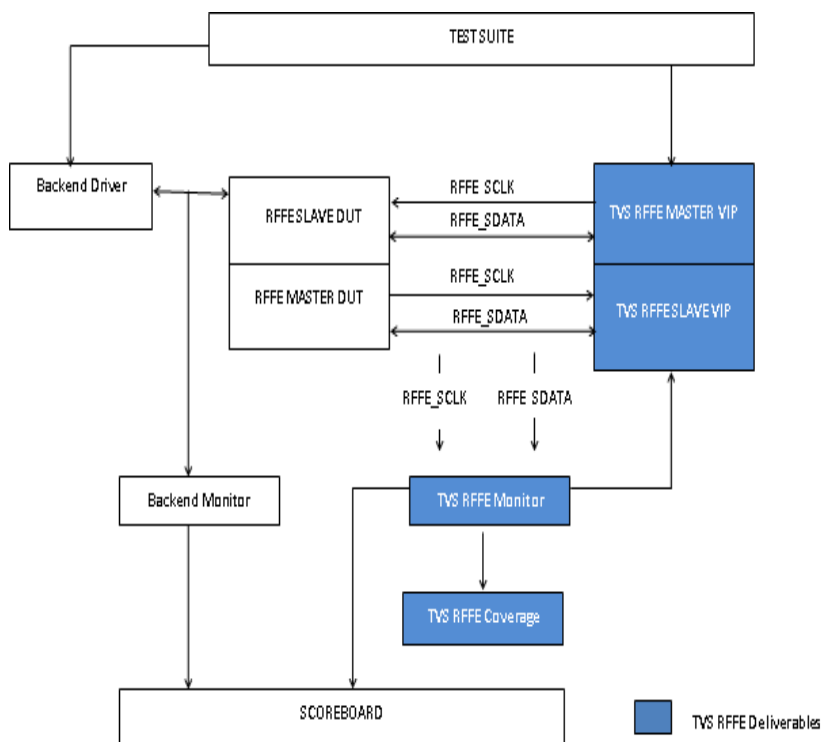
Overview

Test and Verification Solutions offers a Mobile Industry Processor Interface Radio Frequency Front End (RFFE) OVM/UVM VIP as part of its asureVIP™ series of offerings. This is a highly flexible and configurable verification IP, which can be easily integrated in any OVM/UVM SoC environment or Block Level Verification Environments.

The TVS OVM/UVM based MIPI RFFE VIP supports both Master and Slave functionalities. It has been entirely programmed in System Verilog and it provides support for OVM/UVM based testbenches.

The VIP comes with a protocol BUS Monitor which checks for non-compliance with RFFE specification. Monitor will collect the information from the bus and will frame the high level abstraction classes such as command and address/data frames. During the “master passive and slave active” mode configuration, the monitor will collect the information from the bus and inform the slave on what command is initiated from the master and how many bytes are to be transferred from slave to Master.

Block Diagram



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Features

- Supports MIPI RFFE Specification 10.0
- MIPI RFFE Master and Slave Functionality
- Configuration support Master, Slave or Both
- Supports Multiple Slaves
- Supports FULL_SPEED and HALF_SPEED
- Supports following frames:
 - Command Frame
 - Address/Data Frame
 - No Response Frame
- Supports Register 0 write command
- Supports Register Write and Read command
- Supports Extended Register write command
- for single slave

Benefits

- Highly Flexible, Independent and Configurable MIPI RFFE OVM/UVM VIP
- Proven against Silicon Proven VIP
- Less TAT in integrating into SOC Verification environments



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TVS MIPI RFFE OVM/UVM VIP

Technical Specification

Part Number	asureVIP_MIPI_RFFE
Description	MIPI RFFE OVM/UVM VIP
Provider	Test and Verification Solutions
Languages Supported	System Verilog
Methodology	OVM 2.1.1 / UVM 1.1
Simulators	Cadence Incisive, Mentor Questa, Aldec Riviera-PRO
Compliance	MIPI RFFE Specification
Availability	Now

About Us

TVS delivers an independent verification service that not only reduces your costs and time-to-market, but also improves product quality.

TVS combines skills and experience in software testing, hardware verification and outsourcing to provide customers with an efficient, well-managed, quality assurance service.

TVS provides both consultancy and execution services using experienced engineering resources in several locations around the world. TVS removes the pain and risk from outsourcing leaving you with just the benefits.

To learn more about our offerings, write to us at sales@testandverification.com

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April 2014 Version 2.0

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Features

- Supports Extended Register Read command
- Supports Extended Register write long command
- Supports Extended Register Read long command
- Supports Multiple slave address value configuration
- Supports Master and Slave Error Injection schemes such as:
 - Undefined command Frame
 - Command frame with parity error
 - Incompatible command length
 - Address frame with parity error
 - Data frame with parity error
- Read using the broadcast ID

Deliverables

- RFFE Master/Slave OVM/UVM VIP
- RFFE Monitor
- Test Suite, User Guide



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