

Verification Futures

The Next Five Years

15th November 2011

Mike Bartley, TVS



- 8.30** **Arrival: Refreshments and Networking**
- 9.30** **Introduction: Mike Bartley, TVS**
- 9.35** **Harry Foster, Mentor Graphics**
- 10.20** **Panel Session: Our Top Verification Challenges**
 - Bryan Dickman, ARM**
 - Olivier Haller, STMicroelectronics**
 - Hans Lundén, Ericsson**
 - Clemens Müller, Infineon**
- 11.00** **Refreshments and Networking**
- 11.30** **Mike Stellfox, Cadence Design Systems**
- 12.00** **Serrie Chapman and Darren Galpin, Infineon Technologies**
- 12.20** **Mike Benjamin, TVS**
- 12.40** **Steve Holloway, Dialog Semiconductor**

- 13.00 Lunch and Networking**
- 14.00 Lawrence Loh, Jasper Design Automation**
- 14.30 Nick Gatherer, ARM**
- 15.00 Jean-Marc Forey, SpringSoft**
- 15.30 Refreshments and Networking**
- 16.00 Janick Bergeron, Synopsys**
- 16.30 Panel Session: The EDA response**
- 17.15 Refreshments and Networking**



- Refreshments and drinks
- Mobile phones
- Fire
- Parking - Validate ticket

